



Global Communication Semiconductors, LLC

Corporate & Foundry
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Process Integration Engineer - Opto

Job Req: #0772
Department: Opto – R&D
Status: Full-Time/Salaried
Shift: Day
Salary Range: \$70,000 - \$115,000 annually

Job Description

- Experience and knowledge in semiconductor wafer process for Opto components
- Participate in process development and improvement efforts in different areas of the GCS compound semiconductor FAB
- Serves as project engineer to oversee FAB process lots for developments as well as contract deliverables: from mask layout review and lot starts to wafers and test data delivery
- Collects and analyzes device test results
- Conducts review and provides timely reports to Managers
- Trains techs and operators as assigned

Job Requirements

- BS/MS in Physics or Engineering field (Electrical, Mechanical, Chemical, and Material Sciences)
- Must have clean room and hands-on wafer process experience in silicon or III-V compound semiconductors
- Excel and document drafting skills
- Ability to effectively work in a fast-paced environment, with shifting priorities, to support overall business objectives
- Ability to effectively communicate and interact with all other employees at all levels, vendors, customers, and management in a professional manner